

2.2 WIZS2E evaluation board

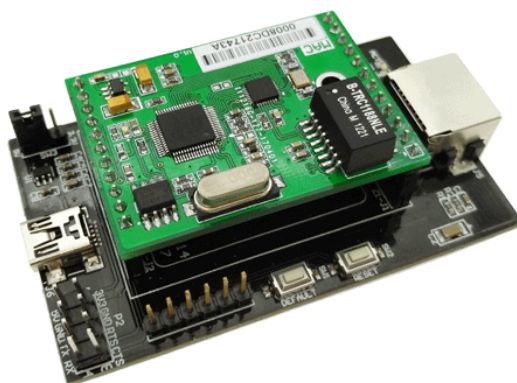
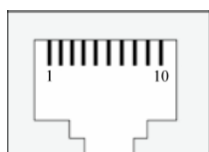


Figure 2-3 WIZS2E evaluation board

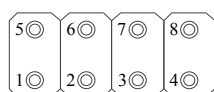
WIZS2E evaluation board provides a simple platform for testing and running an application for WIZS2E modules including W7500S2E-Z1. Where S1-J1 and S1-J2 are used to plug in the W7500S2E-Z1 module, S1-J1 and S1-J2 are used to plug in other models of WIZS2E module (s), which are described in the user manuals of the related modules. The evaluation board integrates RJ45, serial TTL and USB mini interfaces. (The following figure shows “x” means vacant)

- ◆ RJ-45 (J5) interface Pin Assignment



Pin	Signal	Pin	Signal
1	RXIN	6	TXOP
2	RXIP	7	x
3	TXON	8	x
4	x	9	AGND
5	x	10	AGND

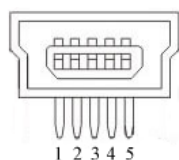
- ◆ TTL interface (P2) Pin Assignment



Pin	Signal	Pin	Signal
1	5V	5	3V3
2	GND	6	GND
3	TX	7	RTS
4	RX	8	CTS

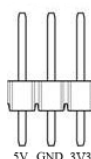
- ◆ USB Mini Interface (J6) Pin Assignment supply 5 V DC power for the evaluation board only.

Note: Not for debug or communication



Pin	Signal	Pin	Signal
1	5V	4	x
2	x	5	GND
3	x		

- ◆ SW3 is power supply pin for other modules, it would be leave open for W7500S2E-Z1



Pin	Signal	Pin	Signal
1	5V	3	3V3
2	GND		

◆ W7500S2E-Z1 evaluation board buttons introduction

Marking	Description
SW1 (DEFAULT)	Press switch for 1-3 seconds for soft reset, over 3 seconds to factory reset
SW2 (RESET)	Hardware reset button

Figure 2-4 W7500S2E-Z1 evaluation board button description

◆ W7500S2E-Z1 evaluation board LED description

Marking	Description
ACT	Ethernet status indicator
LINK	Ethernet connection indicator
DATA	Ethernet data communication LED, the Blink speed shows the data speed

Table 2-5 W7500S2E-Z1 evaluation board LED description

◆ Hardware connection explanation

W7500S2E-Z1 used two sections of pin layouts in 1 x 7 pin and 2 x 7 pin designs. This is to avoid plugging the module in the wrong directing which may damage to the module.

◆ Figure 2-4 shows the reference schematic of the evaluation board for developing reference.

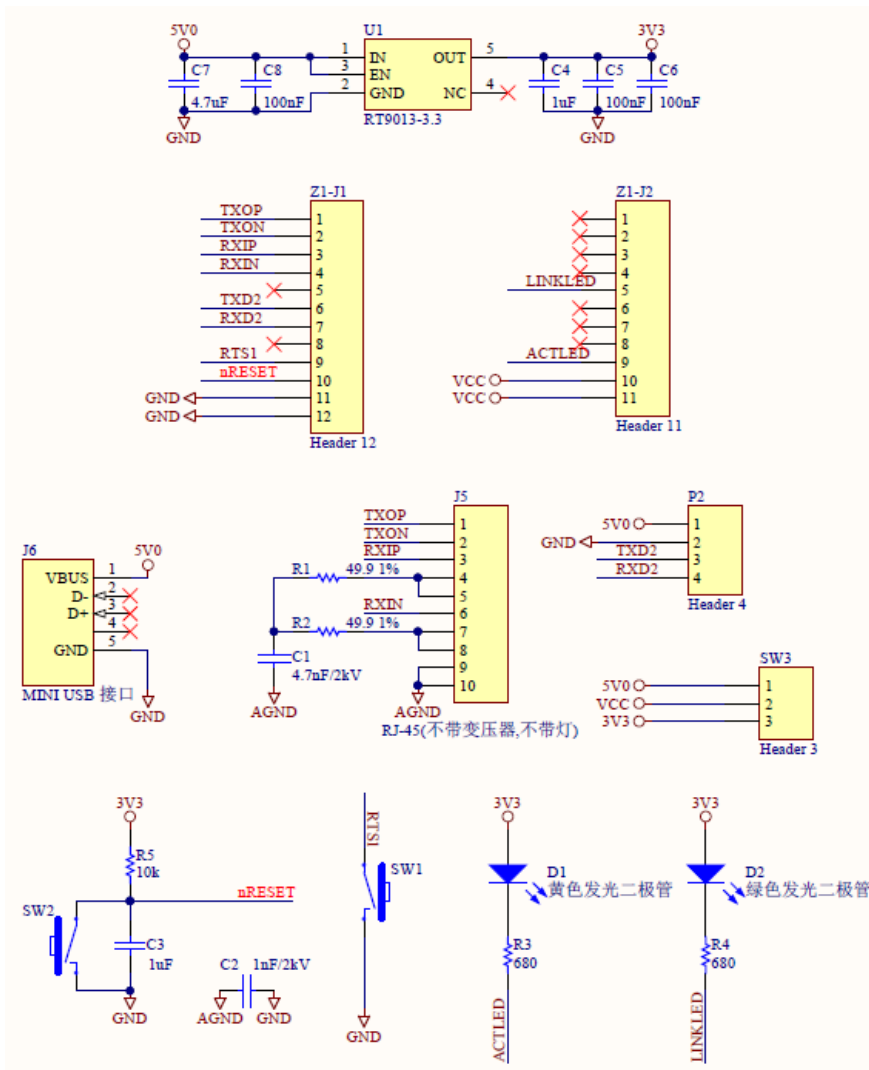


Figure 2-4 W7500S2E-Z1 evaluation board reference schematic